

Fig. 1

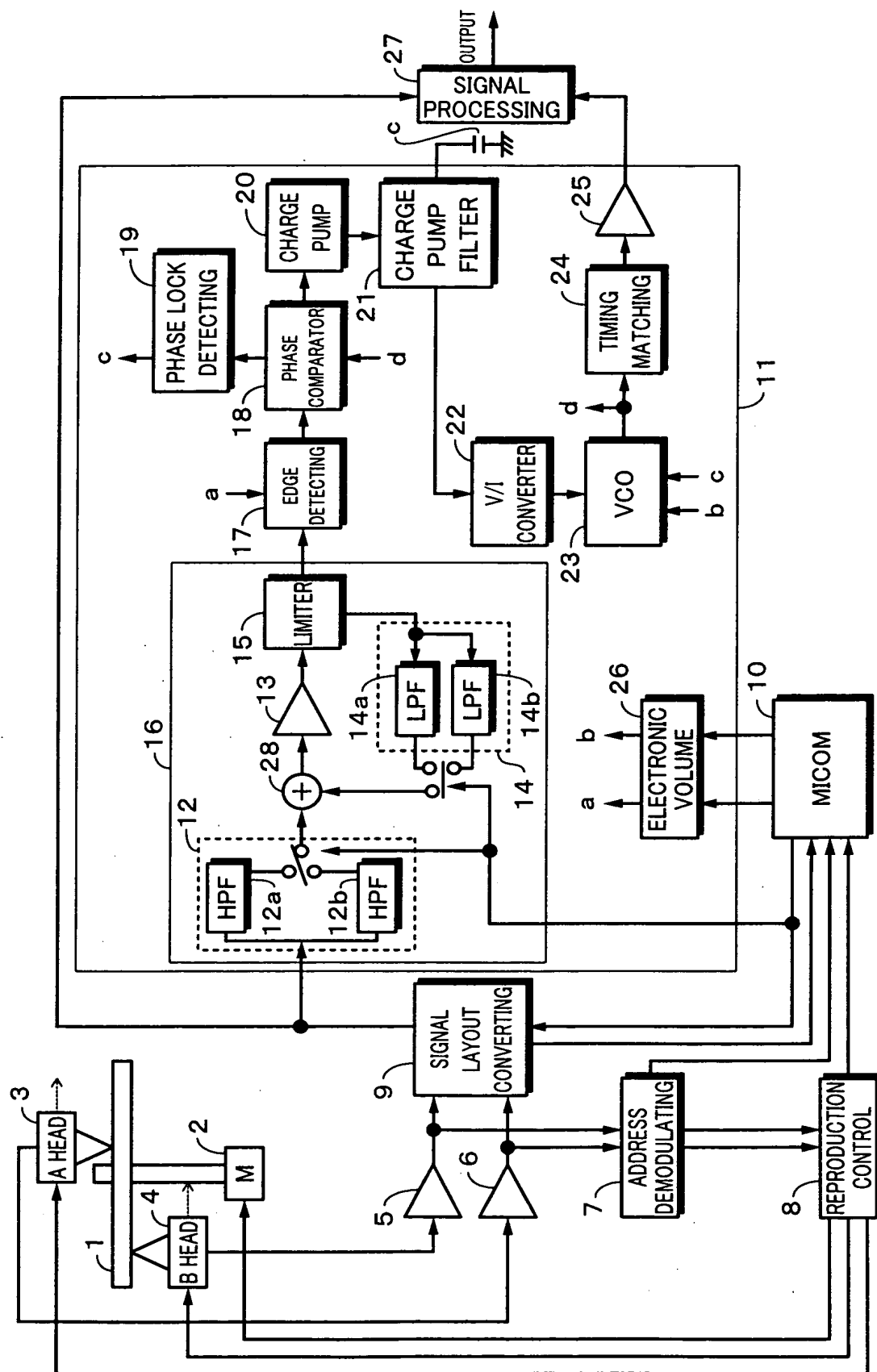


Fig. 2A

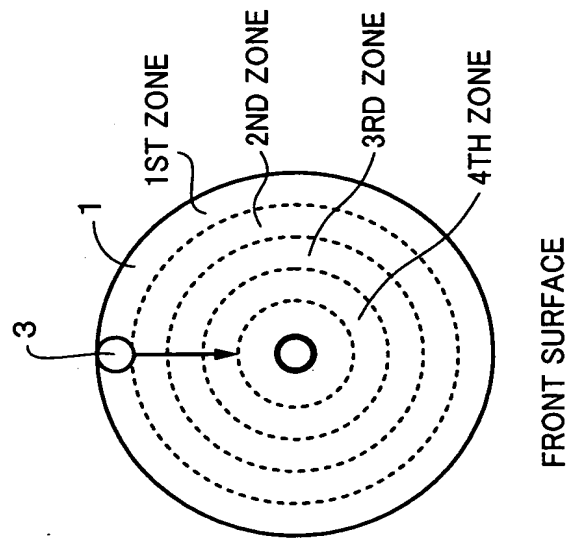


Fig. 2B

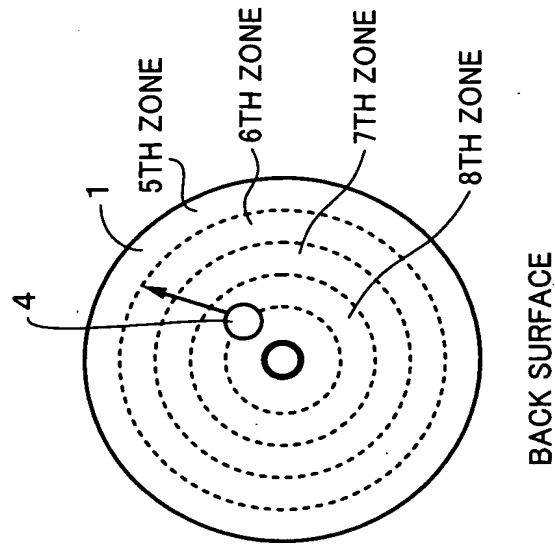


Fig. 3A



Fig. 3B



Fig. 3C



Fig. 3D

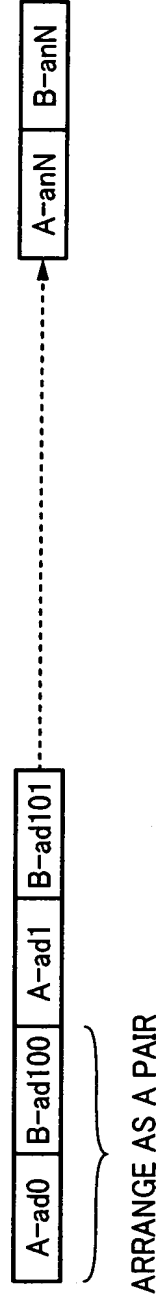
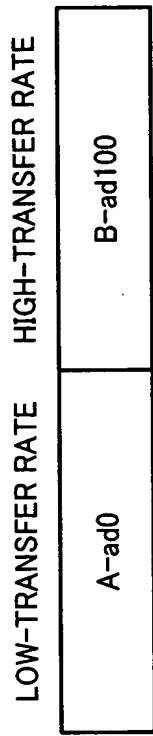


Fig. 4A



A-ad0 SIGNAL → B-ad100 SIGNAL

Bch HEAD SW SIGNAL

Ach HEAD SW SIGNAL



Fig. 4B

Fig. 4C

A-ad0 SIGNAL

B-ad100 SIGNAL



Fig. 4D

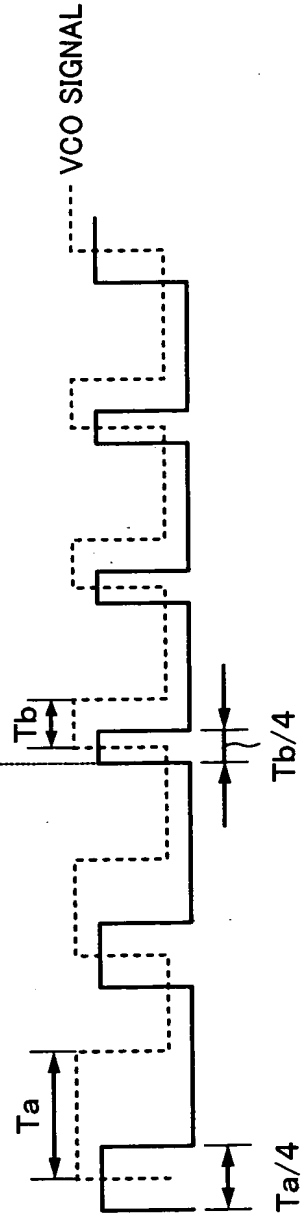


Fig. 4E

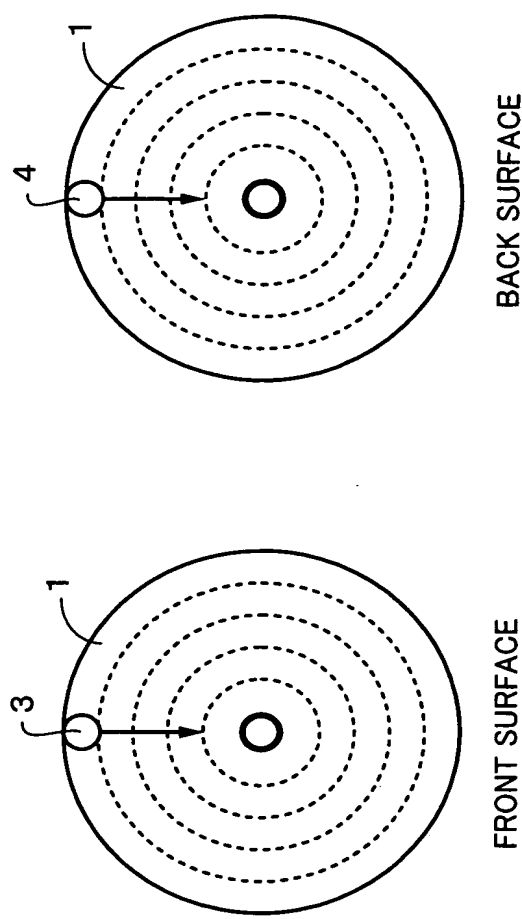


Fig. 5A

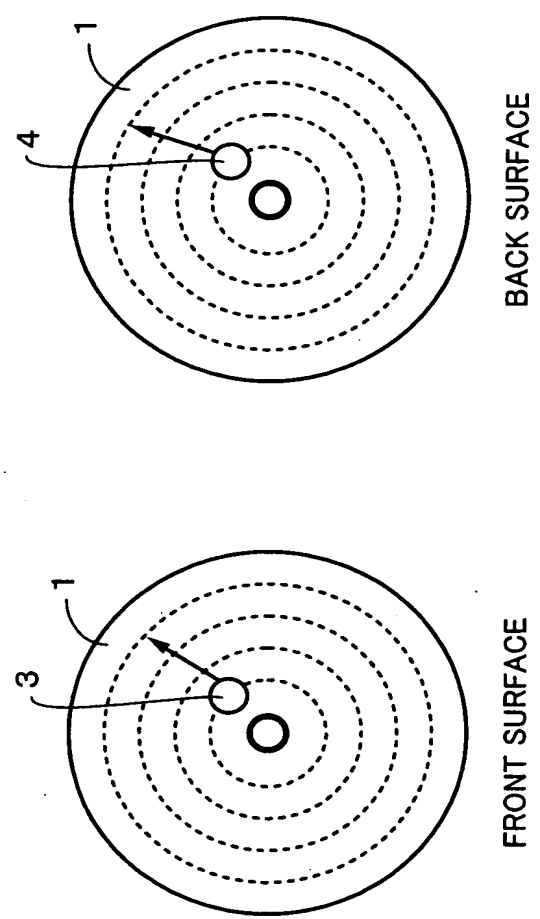


Fig. 5B

DESCRIPTION OF REFERENCE NUMERALS

1	DISC-SHAPED RECORDING MEDIUM
2	SPINDLE MOTOR
3	Ach HEAD
4	Bch HEAD
5, 6	PREAMPLIFIER
7	ADDRESS DEMODULATING UNIT
8	REPRODUCTION CONTROL UNIT
9	SIGNAL LAYOUT CONVERTING CIRCUIT
10	MICROCOMPUTER
11	CLOCK EXTRACTING UNIT
16	WAVEFORM EQUALIZING CIRCUIT
17	EDGE DETECTING CIRCUIT
18	PHASE COMPARATOR
19	PHASE LOCK DETECTING CIRCUIT
20	CHARGE PUMP
21	CHARGE PUMP FILTER
23	VOLTAGE CONTROLLED OSCILLATOR
26	ELECTRONIC VOLUME
27	SIGNAL PROCESSING UNIT